

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,983,538 B2
APPLICATION NO. : 10/696598
DATED : January 10, 2006
INVENTOR(S) : Masafumi Inoue et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6

Lines 21-49, claims 4-7, delete

“4. A method of mounting components on a board, said method comprising the steps of:

- a) receiving mounting locations of where said components are intended to be mounted on said board,
- b) determining where solder for said components is situated on said board,
- c) determining where said components are to be mounted on said board based on steps a) and b); and
- d) mounting said components on said board based to step c).

5. A method of mounting components according to claim 4, wherein step c) includes the step of modifying said mounting locations based on step b), wherein said components are mounted in step d) based on said modified mounting locations.

6. Apparatus for mounting components on a board, comprising:
a memory in which is situated location values which correspond to where components are intended to be mounted on said board;
a locator for identifying solder locations which correspond to respective locations of solder on said board;
a mounter for mounting said components based on said location values and said solder locations.

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7. Apparatus for mounting components according to claim 6, further comprising a calculation unit which modifies said location values based on said solder values, wherein said mounter mounts said components based on said modified location values.”

Signed and Sealed this

Twenty-fifth Day of December, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office